

ALLEGATO A

UNIVERSITÀ DEGLI STUDI DI MILANO

Procedura di selezione per la chiamata a professore di I fascia da ricoprire ai sensi dell'art. 18, comma 1, della Legge n. 240/2010 per il settore concorsuale 01/B1 - INFORMATICA,
(settore scientifico-disciplinare INF/01 - INFORMATICA)
presso il Dipartimento di Informatica Giovanni Degli Antoni,
(avviso bando pubblicato sulla G.U. n. 59 del 26/07/2022) - Codice concorso 5029

Tommaso Cucinotta **CURRICULUM VITAE**

INFORMAZIONI PERSONALI (NON INSERIRE INDIRIZZO PRIVATO E TELEFONO FISSO O CELLULARE)

COGNOME	CUCINOTTA
NOME	TOMMASO
DATA DI NASCITA	25/04/1974

TITOLI

TITOLO DI STUDIO

(indicare la Laurea conseguita inserendo titolo, Ateneo, data di conseguimento, ecc.)

Laurea quinquennale (vecchio ordinamento) in Ingegneria Informatica, conseguita il 3/05/2000 presso l'Università degli Studi di Pisa con votazione di 110/110 e lode.

TITOLO DI DOTTORE DI RICERCA O EQUIVALENTI, OVVERO, PER I SETTORI INTERESSATI, DEL DIPLOMA DI SPECIALIZZAZIONE MEDICA O EQUIVALENTE, CONSEGUITO IN ITALIA O ALL'ESTERO

(inserire titolo, ente, data di conseguimento, ecc.)

Perfezionamento in Ingegneria Industriale e dell'Informazione, Area "Sistemi Informativi per la Pubblica Amministrazione e le Imprese: Sistemi di Workflow e di Firma Digitale", conseguito il 21/07/2004 presso la Scuola Superiore Sant'Anna di Pisa con votazione di 100/100 e lode, equiparato al Dottorato di Ricerca ai sensi della Legge 14 Febbraio 1987, n. 41, Art. 2.

POSSESSO REQUISITO ASN (Abilitazione Scientifica Nazionale)

- ASN I fascia nel Settore concorsuale 01/B1, conseguita il 23/11/2020, scade il 23/11/2029
- ASN I fascia nel Settore Concorsuale 09/H1, conseguita il 27/07/2018, nel Quinto Quadrimestre Scade il 26/07/2027 (in origine 26/07/2024, durata estesa a 9 anni dal DL n.126/2019)

ALTRI TITOLI CONSEGUITI

(inserire titolo, ente, data di conseguimento, ecc.)

- Lug 2019 - oggi: **Coordinatore dell'area Real-Time & Embedded Systems** presso l'Istituto TECIP della Scuola Superiore Sant'Anna
- Dicembre 2015 - oggi: **Professore Associato** presso la Scuola Superiore Sant'Anna di Pisa
- Gen 2012 - Giu 2014: **Ricercatore presso i prestigiosi Bell Labs**, Alcatel-Lucent, Dublin (Ireland)
- Oct 2011 - Sep 2013: Contratto di Co.Co.Co. per collaborazione al progetto di ricerca su Qualità del Servizio per Applicazioni Service-Oriented Virtualizzate, Scuola Superiore Sant'Anna.
- Nov 2008 - Gen 2009: Collaborazione coordinata e continuativa con l'Università degli Studi di Trento per attività di ricerca su "Studio e realizzazione di un meccanismo di security per applicazioni di controllo distribuito" all'interno del progetto Europeo CHAT (FP7 ICT 224428)
- Gen 2008: Prestazione occasionale per Evidence srl per attività di ricerca all'interno del progetto

- Europeo INTEREST (IST 33661)
- Ott 2007: Prestazione occasionale per Evidence srl per attività di ricerca all'interno del progetto Europeo INTEREST (IST 33661)
- Gen 2007: Prestazione occasionale per Evidence srl per attività di ricerca all'interno del progetto Europeo INTEREST (IST 33661)
- Gen 2005 - Dic 2011: Ricercatore a Tempo Determinato presso la Scuola Superiore Sant'Anna
- Gen 2001 - Lug 2001: Collaborazione professionale con il Consorzio Nazionale delle Ricerche (CNR) per attività di ricerca all'interno del progetto di ricerca SCHOLNET
- Jul 2001 - Oct 2001: Research Internship for the design and realization of the MuscleCard open-source architecture for smart-cards on Linux, in Schlumberger, Austin, TX.

ESPERIENZE INDUSTRIALI DI SVILUPPO SOFTWARE

- Lug 2014 - Dicembre 2015: Senior Software Development Engineer nel team di AWS DynamoDB, presso Amazon Database Services, Dublin (Ireland)
- Gen 2012 - Giu 2014: Ricercatore presso i prestigiosi Bell Labs, Alcatel-Lucent, Dublin (Ireland)
- Feb 2009: Collaborazione occasionale come sviluppatore software, Evidence srl

ATTIVITÀ DIDATTICA

INSEGNAMENTI E MODULI

(inserire periodo [gg/mm/aa inizio e fine], anno accademico, corso laurea, numero di ore frontali, eventuale CFU)

1. Annualmente, dall'a.a. 2016/17 ad oggi: Corsi su "Cloud Computing and Big-Data" (30 ore, 3 CFU) e "Cloud Computing and Big-Data Lab" (30 ore, 3 CFU) nel PhD in Emerging Digital Technologies presso la Scuola Superiore Sant'Anna, nel PhD in Data Science presso la Scuola Normale Superiore di Pisa, e il Dottorato Nazionale in Artificial Intelligence presso l'University of Pisa. Corsi tenuti interamente in lingua Inglese
2. a.y. 2021/22: Real-Time and High-Performance Computing on Linux (20 ore, 2 CFU), PhD in Emerging Digital Technologies at Scuola Superiore Sant'Anna
3. a.y. 2021/22: Java Programming Fundamentals (30 ore), Course for Honor Students at Scuola Superiore Sant'Anna
4. a.y. 2020/21: Computer Security (30 ore), Course for Honor Students at Scuola Superiore Sant'Anna
5. a.y. 2020/21: Advanced Java Programming (30 ore), Course for Honor Students at Scuola Superiore Sant'Anna
6. a.y. 2020/21: C/C++ Programming Foundations (10 ore), Course for Honor Students at Scuola Superiore Sant'Anna
7. Yearly, from a.y. 2016/17 to 2019/20: Course on "Component-Based Software Design," (titolare del corso, 90 ore totali, 9 CFU, di cui 70 ore assegnate al sottoscritto) MSc degree in Embedded Computing Systems jointly organized by Scuola Superiore Sant'Anna and University of Pisa. Course language: English
8. Dic 2016: 7 giornate di formazione sul tema "Cloud Computing" per il prestigioso United Technology Research Center (UTRC), Roma
9. March 2016: Course on "From Classical Distributed Systems to Cloud Computing" (30 ore), for PhD students at the Computer Engineering Dept. of University of Pisa. Course Language: English
10. Yearly, from 2005 to 2010: Course on "Design of Embedded System", Faculty of Engineering, University of Siena
11. Yearly, from 2003 to 2011: Course on "System Security and Cryptography", International Master on Information Technology, Scuola Superiore Sant'Anna. Course language: English.
12. Yearly, from 2005 to 2011: Course on "System Security and Cryptography", International Master on Communication Networks Engineering, Scuola Superiore Sant'Anna. Course language: English.
13. Jun 2008: Course on "Real-time operating systems" for the "UNI-NORGE Project", Ericsson, Rome.
14. Mar - May 2006: Course on "Design of Embedded Systems", International Master on Information Technology - Scuola Superiore Sant'Anna. Course language: English
15. Oct - Nov 2005: Course on "Java", International Master on Information Technology and Communication Networks Engineering, Scuola Superiore Sant'Anna. Course language: English
16. Oct - Nov 2005: Corso "Calcolatori Elettronici", Faculty of Engineering, University of Siena.

17. Oct 2005 - Sep 2006: Course on Industrial Informatics, Faculty of Engineering, University of Siena.
18. Oct - Dec 2005: Supporto alla Didattica per il corso d'Informatica, Facoltà d'Ingegneria, Università di Pisa
19. Oct - Nov 2005: Course on "Computer Architectures I", Faculty of Engineering, University of Siena.
20. May - Jul 2005: Course on Industrial Informatics, Faculty of Engineering, University of Siena.
21. Oct - Dec 2004: Supporto alla Didattica per il corso d'Informatica, Facoltà d'Ingegneria, Università di Pisa
22. Feb - May 2004: Supporto alla Didattica per il corso d'Informatica, Facoltà d'Ingegneria, Università di Pisa
23. Apr 2002: Corso su "System Security and Cryptography", International Master in Software Engineering, Scuola Superiore Sant'Anna.
24. Nov 2017 - Giu 2018: Seminari su "Privacy by design and privacy by default: enabling technologies" e "Data privacy in the big-data era: data harvesting and the «private» citizen", Corso di Alta Formazione Abilitante Responsabile Protezione Dati, Scuola Superiore Sant'Anna
25. Giu 2006: Corso su "Il quadro tecnologico e giuridico: terminologia, concetti di base e problematiche tecnologiche", Corso di Alta Formazione Codice dell'amministrazione digitale, Scuola Superiore Sant'Anna
26. Mar 2006: Lezione su "Tecnologie per la sicurezza nell'e-Health", Master in European Procurement and Transplantation Programs Management, Scuola Superiore Sant'Anna
27. Oct 2005: Corso su "Quadro tecnologico e giuridico: terminologia, concetti di base e problematiche tecnologiche", Corso di Alta Formazione Codice dell'amministrazione digitale, Scuola Superiore Sant'Anna
28. Apr 2004: Corso su "Il commercio elettronico: problematiche tecnologiche per l'autenticazione, la sicurezza, la riservatezza", Corso di Alta Formazione su Marketing e Distribuzione di Prodotti Assicurativi, IV ed., Scuola Superiore Sant'Anna.
29. Nov 2005: Corso sul tema "Basic di Informatica", Master in Management dell'Innovazione, Scuola Superiore Sant'Anna.
30. May 2001: Incarico di docenza sul tema "Tecnologie e strumenti per la sicurezza informatica", Ciclo di Seminari sulle Tecnologie per la Pubblica Amministrazione, Scuola Superiore Sant'Anna.
31. May 2001: Corso di formazione facoltativa su "Protocollo Informatico", Scuola Superiore Sant'Anna.
32. Apr 2001: Corso di specializzazione su "Protocollo Informatico: nozioni specialistiche per esperti del settore", Scuola Superiore Sant'Anna.

ATTIVITÀ DI DIDATTICA INTEGRATIVA E DI SERVIZIO AGLI STUDENTI

ATTIVITÀ DI TUTORATO DEGLI STUDENTI DI CORSI DI LAUREA, DI LAUREA MAGISTRALE E DOTTORATO, E ATTIVITÀ DI RELATORE DI ELABORATI DI LAUREA, DI TESI DI LAUREA MAGISTRALE, E DI TESI DI DOTTORATO

Tutoraggio di dottorandi di ricerca

1. Remo Andreoli, PhD in Emerging Digital Technologies at Scuola Superiore Sant'Anna, 2021-oggi
2. Filippo Galli, PhD in Data Science at Scuola Superiore Sant'Anna jointly with others, 2020 - oggi
3. Giacomo Lanciano, PhD in Data Science at Scuola Superiore Sant'Anna jointly with other, 2019 - oggi
4. Gabriele Ara, PhD in Emerging Digital Technologies at Scuola Superiore Sant'Anna, 2019 - oggi
5. Daniel Bristot de Oliveira, PhD in Emerging Digital Technologies at Scuola Superiore Sant'Anna, 2017-2020, in co-tutela con il prof. R. S. de Oliveira, Universidad Federale de Santa Catarina (UFSC)
6. Agostino Mascitti, PhD in Emerging Digital Technologies at Scuola Superiore Sant'Anna, 2018-2021
7. Alessio Balsini, PhD in Emerging Digital Technologies at Scuola Superiore Sant'Anna, 2016-2018

Tutoraggio di studenti di Laurea Magistrale durante lo svolgimento della Tesi di Laurea

1. Yohannis Kifle Telila, Laurea Magistrale in Informatica, Università di Pisa, 2022 - oggi
2. Dawit Mezemir Anelay, Laurea Magistrale in Informatica, Università di Pisa, 2022 - oggi
3. Remo Andreoli, Tesi per Laurea Magistrale in Informatica, Università di Pisa, 2021
4. Leonardo Lai, Tesi per Laurea Magistrale in Embedded Computing Systems (Ingegneria Informatica), Scuola Superiore Sant'Anna in collaborazione con l'Università di Pisa, 2021
5. Jacopo Malvatani, Tesi per Laurea Magistrale in Embedded Computing Systems (Ingegneria Informatica), Scuola Superiore Sant'Anna in collaborazione con l'Università di Pisa, 2021

6. Stefano Fiori, Tesi per Laurea Magistrale in Embedded Computing Systems (Ingegneria Informatica), Scuola Superiore Sant'Anna in collaborazione con l'Università di Pisa, 2020
7. Gabriele Ara, Tesi per Laurea Magistrale in Embedded Computing Systems (Ingegneria Informatica), Scuola Superiore Sant'Anna in collaborazione con l'Università di Pisa, 2019
8. Andrea Stevanato, Tesi per Laurea Magistrale in Embedded Computing Systems (Ingegneria Informatica), Scuola Superiore Sant'Anna in collaborazione con l'Università di Pisa, 2019
9. Elena Luccherini, Tesi per Laurea Magistrale in Embedded Computing Systems (Ingegneria Informatica), Scuola Superiore Sant'Anna in collaborazione con l'Università di Pisa, 2017
10. Andrea Angella, Tesi per Laurea Magistrale in Ingegneria Informatica, Università di Pisa, 2009
11. Dario Faggioli, Tesi per Laurea Magistrale in Ingegneria Informatica, Università di Pisa, 2008

Tutoraggio di Allievi Ordinari (AO) della Scuola Superiore Sant'Anna (Honor Students)

1. Antonio Napolitano, AO in Ingegneria Informatica, 2021 - oggi
2. Pietro Francaviglia, AO in Ingegneria Informatica, 2018 - oggi
3. Giorgio Donati, AO in Ingegneria Informatica, 2018 - oggi
4. Ayoub El Ourrak, AO in Ingegneria Informatica, 2017 - oggi
5. Riccardo Mancini, AO in Ingegneria Informatica, 2016 - oggi

SEMINARI

(inserire titolo del seminario, luogo, data, ecc.)

- 21/09/2021: Biometrics & multi-factor authentication, Seasonal School on Cybersecurity - a multidisciplinary perspective, Scuola Superiore Sant'Anna
- 24/09/2021: Crittografia e sicurezza dei dati, Seasonal School on Cybersecurity - a multidisciplinary perspective, Scuola Superiore Sant'Anna
- 24/02/2022: Biometrics & multi-factor authentication, Seasonal School on Cybersecurity - a multidisciplinary perspective, Scuola Superiore Sant'Anna
- 25/02/2022: Crittografia e sicurezza dei dati, Seasonal School on Cybersecurity - a multidisciplinary perspective, Scuola Superiore Sant'Anna
- 15/09/2021: Cryptography and data security, Seasonal School on Cyber-Physical Systems
- 17/09/2021: Component-based software, Seasonal School on Cyber-Physical Systems
- 23/03/2018: Privacy by design and privacy by default: enabling technologies, Corso di Alta Formazione per Data Protection Officer (DPO), Scuola Superiore Sant'Anna
- 23/03/2018: Data privacy in the big-data era: data harvesting and the citizen, Corso di Alta Formazione per Data Protection Officer (DPO), Scuola Superiore Sant'Anna
- 24/11/2017: Privacy by design and privacy by default: enabling technologies, Corso di Alta Formazione per Data Protection Officer (DPO), Scuola Superiore Sant'Anna
- 24/11/2017: Data privacy in the big-data era: data harvesting and the citizen, Corso di Alta Formazione per Data Protection Officer (DPO), Scuola Superiore Sant'Anna
- Jun 2006: Lecture on "Digital Signatures: technological and juridical framework", Specialization course "Codice dell'Amministrazione Digitale D. Lgs. n.82/2005", LiderLab, Scuola Superiore Sant'Anna
- Mar 2006: Lecture on "Technologies for e-Health Security", Master on European Procurement and Transplantation Programs Management, Scuola Superiore Sant'Anna
- Nov 2005: Lecture on "Informatics Basics", Master on Innovation Management, Scuola Superiore Sant'Anna
- Oct 2005: Specialization course "Codice dell'Amministrazione Digitale D. Lgs. n.82/2005" -- Lecture on "Digital Signature: a technological viewpoint", LiderLab, Scuola Superiore Sant'Anna
- Apr 2004: Course on "e-commerce: technological issues related to authentication, security and privacy" within the 2004 Edition of the Specialization Course "Marketing e Distribuzione di Prodotti Assicurativi", Scuola Superiore Sant'Anna
- Apr - May 2001: Specialization course "The Electronic Registry: specialistic knowledge for experts", Scuola Superiore Sant'Anna
- May 2001: Seminar "Technologies for innovating the Public Administration - Technologies and tools for ICT security", Scuola Superiore Sant'Anna

ATTIVITÀ DI RICERCA SCIENTIFICA

PUBBLICAZIONI SCIENTIFICHE

(per ciascuna pubblicazione indicare: nomi degli autori, titolo completo, casa editrice, data e luogo di pubblicazione, codice ISBN, ISSN, DOI o altro equivalente)

Le 12 pubblicazioni allegate alla domanda sono elencate nell'allegato apposito. Qui elenco invece tutte le pubblicazioni, che sono accessibili ad es. su:

<http://retis.sssup.it/~tommaso/ita/publications.html>

Pubblicazioni su proceeding di conferenze e workshop internazionali

1. T. Cucinotta, L. Pannocchi, F. Galli, S. Fichera, S. Lahiri, A. Artale. "[Optimum VM Placement for NFV Infrastructures](#)," in Proceedings of the [10th IEEE International Conference on Cloud Engineering \(IC2E 2022\)](#), September 26-30, 2022, Pacific Grove, CA, USA.
2. L. Abeni, T. Cucinotta, B. Pinczel, P. Matray, M. Srinivasan, T. Lindquist. "[On the use of Linux Real-Time Features for RAN Packet Processing in Cloud Environments](#)," in Proceedings of the 17th Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2022), International Supercomputing Conference - High Performance (ISC 2022), June 2nd, 2022, Hamburg, Germany.
3. S. Fiori, L. Abeni, T. Cucinotta. "[RT-Kubernetes - Containerized Real-Time Cloud Computing](#)," in Proceedings of the 37th ACM/SIGAPP International Symposium on Applied Computing (ACM SAC 2022), April 25-29, 2022, Brno, Czech Republic (on-line event due to Covid-19).
4. G. Ara, T. Cucinotta, A. Mascitti. "[Simulating Execution Time and Power Consumption of Real-Time Tasks on Embedded Platforms](#)," in Proceedings of the 37th ACM/SIGAPP International Symposium on Applied Computing (ACM SAC 2022), April 25-29, 2022, Brno, Czech Republic (on-line event due to Covid-19).
5. G. Lanciano, F. Galli, T. Cucinotta, D. Bacciu, A. Passarella. "[Predictive Auto-scaling with OpenStack Monasca](#)," in Proceedings of the 14th IEEE/ACM International Conference on Cloud Computing (IEEE/ACM UCC 2021), December 6-9th, 2021, Leicester, UK.
6. L. Lai, G. Ara, T. Cucinotta, K. Kondepu, L. Valcarengi. "[Ultra-low Latency NFV Services Using DPDK](#)," in Proceedings of the 7th IEEE Conference on Network Functions Virtualization and Software-Defined Networking (IEEE NFV-SDN 2021), November 9-11th, 2021, Heraklion, Greece (on-line event due to Covid-19).
7. R. Andreoli, T. Cucinotta. "[Differentiated performance in NoSQL database access for Cloud HPC workloads](#)," in Proceedings of the 16th Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2021), International Supercomputing Conference - High Performance (ISC 2021), July 2nd, 2021, Frankfurt, Germany (on-line event due to Covid-19).
8. A. Stevanato, T. Cucinotta, L. Abeni, D. B. de Oliveira. "[An Evaluation of Adaptive Partitioning of Real-Time Workloads on Linux](#)," in Proceedings of the 24th IEEE International Symposium on Real-Time Distributed Computing (IEEE ISORC 2021), June 1-3, 2021, Daegu, South Korea (on-line event due to Covid-19).
9. T. Cucinotta, G. Lanciano, A. Ritacco, F. Brau, F. Galli, V. Iannino, M. Vannucci, A. Artale, J. Barata, E. Sposato. "[Forecasting Operation Metrics for Virtualized Network Functions](#)," in Proceedings of the 21st IEEE/ACM International Symposium on Cluster, Cloud and Internet Computing (IEEE CCGRID 2021), May 10-13, 2021, Melbourne, Victoria, Australia (on-line event due to Covid-19).
10. R. Andreoli, T. Cucinotta, D. Pedreschi. "[RT-MongoDB: a NoSQL database with differentiated performance](#)," in Proceedings of the 11th International Conference on Cloud Computing and Services Science (CLOSER 2021), April 28-30, 2021, Prague, Czech Republic (on-line event due to Covid-19). [Best Student Paper Award](#)
11. T. Cucinotta, L. Abeni. "[Migrating Constant Bandwidth Servers on Multi-Cores](#)," in Proceedings of the 29th International Conference on Real-Time Networks and Systems (RTNS 2021), April 7-9, 2021, Nantes, France (on-line event due to Covid-19).
12. A. Mascitti, T. Cucinotta. "[Dynamic Partitioned Scheduling of Real-Time DAG Tasks on ARM big.LITTLE Architectures](#)," in Proceedings of the 29th International Conference on Real-Time Networks and Systems (RTNS 2021), April 7-9, 2021, Nantes, France (on-line event due to Covid-19).
13. R. Mancini, A. Ritacco, G. Lanciano, T. Cucinotta. "[XPySom: High-Performance Self-Organizing Maps](#)," in Proceedings of the 32nd IEEE International Symposium on Computer Architecture and High Performance Computing (IEEE SBAC-PAD 2020), September 8-11, 2020, Porto, Portugal (on-line event due to Covid-19).

14. D. B. de Oliveira, D. Casini, R. S. de Oliveira, T. Cucinotta. "[Demystifying the Real-Time Linux Scheduling Latency](#)," in Proceedings of the 32nd Euromicro Conference on Real-Time Systems (ECRTS 2020), July 7-10, 2020, Modena, Italy.
15. E. Quiñones, S. Royuela, C. Scordino, L. M. Pinho, T. Cucinotta, B. Forsberg, A. Hamann, D. Ziegenbein, P. Gai, A. Biondi, L. Benini, J. Rollo, H. Saoud, R. Soulat, G. Mando, L. Rucher, L. Nogueira. "[The AMPERE Project: A Model-driven development framework for highly Parallel and EneRgy-Efficient computation supporting multi-criteria optimization](#)," in Proceedings of the 23rd IEEE International Symposium on Real-Time Distributed Computing (IEEE ISORC 2020), May 19-21, 2020, Nashville, Tennessee, USA.
16. A. Mascitti, T. Cucinotta, L. Abeni. "[Heuristic partitioning of real-time tasks on multi-processors](#)," in Proceedings of the 23rd IEEE International Symposium on Real-Time Distributed Computing (IEEE ISORC 2020), May 19-21, 2020, Nashville, Tennessee, USA.
17. G. Serra, G. Ara, P. Fara, T. Cucinotta. "[An Architecture for Declarative Real-Time Scheduling on Linux](#)," in Proceedings of the 23rd IEEE International Symposium on Real-Time Distributed Computing (IEEE ISORC 2020), May 19-21, 2020, Nashville, Tennessee, USA.
18. R. Mancini, T. Cucinotta, L. Abeni. "[Performance Modeling in Predictable Cloud Computing](#)," in Proceedings of the 10th International Conference on Cloud Computing and Services Science (CLOSER 2020), May 7-9, 2020, Prague, Czech Republic.
19. G. Ara, L. Lai, T. Cucinotta, L. Abeni, C. Vitucci. "[A Framework for Comparative Evaluation of High-Performance Virtualized Networking Mechanisms](#)," in 10th International Conference on Cloud Computing and Services Science (CLOSER 2020), Communications in Computer and Information Science (CCIS), Vol. 1399, Springer, Cham.
20. G. Ara, T. Cucinotta, L. Abeni, C. Vitucci. "[Comparative Evaluation of Kernel Bypass Mechanisms for High-Performance Inter-Container Communications](#)," in Proceedings of the 10th International Conference on Cloud Computing and Services Science (CLOSER 2020), May 7-9, 2020, Prague, Czech Republic. [Best Paper Award](#)
21. G. Lanciano, A. Ritacco, F. Brau, T. Cucinotta, M. Vannucci, A. Artale, J. Barata, E. Sposato. "[Using Self-Organizing Maps for the Behavioral Analysis of Virtualized Network Functions](#)," in 10th International Conference on Cloud Computing and Services Science (CLOSER 2020), Communications in Computer and Information Science (CCIS), Vol. 1399, Springer, Cham.
22. T. Cucinotta, G. Lanciano, A. Ritacco, M. Vannucci, A. Artale, J. Barata, E. Sposato, L. Basili. "[Behavioral analysis for Virtualized Network Functions: a SOM-based approach](#)," in Proceedings of the 10th International Conference on Cloud Computing and Services Science (CLOSER 2020), May 7-9, 2020, Prague, Czech Republic.
23. G. Lanciano, A. Ritacco, T. Cucinotta, M. Vannucci, A. Artale, L. Basili, E. Sposato. "[SOM-based behavioral analysis for virtualized network functions](#)," in Proceedings of the 35th ACM/SIGAPP Symposium On Applied Computing (ACM SAC 2020), March 30th, 2020, Brno, Czech Republic.
24. L. Abeni, T. Cucinotta. "[Adaptive Partitioning of Real-Time Tasks on Multiple Processors](#)," in Proceedings of the 35th ACM/SIGAPP Symposium On Applied Computing (ACM SAC 2020), March 30th, 2020, Brno, Czech Republic.
25. C. Vitucci, T. Cucinotta, R. Mancini, L. Abeni. "[Implementation and Deployment of a Server at the Edge Using OpenStack Components](#)," in Proceedings of the International Symposium on Advances in Software Defined Networking and Network Functions Virtualization (SOFTNETWORKING 2020), 19th International Conference on Networks (ICN 2020), February 23-27, 2020, Lisbon, Portugal. [Best Paper Award](#)
26. R. Mancini, S. Tuli, T. Cucinotta, R. Buyya. "[iGateLink: A Gateway Library for Linking IoT, Edge, Fog and Cloud Computing Environments](#)," in Proceedings of the International Conference on Intelligent and Cloud Computing (ICICC 2019), December 16-17, 2019, Bhubaneswar, India.
27. A. Mascitti, T. Cucinotta, M. Marinoni. "[An adaptive, utilization-based approach to schedule real-time tasks for ARM big.LITTLE architectures](#)," in Proceedings of the International Workshop on Embedded Operating Systems (EWILI 2019), October 17th, 2019, New York, USA.
28. D. B. de Oliveira, T. Cucinotta, R. S. de Oliveira. "[Efficient formal verification for the Linux kernel](#)," 17th International Conference on Software Engineering and Formal Methods (SEFM 2019), September 16-20th, 2019, Oslo, Norway.
29. G. Ara, L. Abeni, T. Cucinotta, C. Vitucci. "[On the use of kernel bypass mechanisms for high-performance inter-container communications](#)," in Proceedings of the 14th Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2019), International Supercomputing Conference - High Performance (ISC 2019), June 20th, 2019, Frankfurt, Germany.

30. T. Cucinotta, L. Abeni, M. Marinoni, A. Balsini, C. Vitucci. "[Reducing Temporal Interference in Private Clouds through Real-Time Containers](#)," in Proceedings of the 2019 IEEE International Conference on Edge Computing (IEEE EDGE 2019), July 8-13, 2019, Milan, Italy.
31. D. B. de Oliveira, R. S. de Oliveira, T. Cucinotta. "[Untangling the Intricacies of Thread Synchronization in the PREEMPT RT Linux Kernel](#)," in Proceedings of the 22nd IEEE International Symposium on Real-Time Distributed Computing (IEEE ISORC 2019), May 7-9, 2019, Valencia, Spain.
32. C. Vitucci, L. Abeni, T. Cucinotta, M. Marinoni. "[The Strategic Role of Inter-Container Communications in RAN Deployment Scenarios](#)," in Proceedings of the International Symposium on Advances in Software Defined Networking and Network Functions Virtualization (SOFTNETWORKING 2019), 18th International Conference on Networks (ICN 2019), March 24-28, 2019, Valencia, Spain. **Best Paper Award**
33. A. Balsini, L. Pannocchi, T. Cucinotta. "[Modeling and simulation of power consumption and execution times for real-time tasks on embedded heterogeneous architectures](#)," in Proceedings of the International Workshop on Embedded Operating Systems (EWILI 2018), October 10th, 2018, Torino, Italy.
34. D. B. de Oliveira, T. Cucinotta, R. S. de Oliveira. "[Modeling the Behavior of Threads in the PREEMPT RT Linux Kernel Using Automata](#)," in Proceedings of the International Workshop on Embedded Operating Systems (EWILI 2018), October 10th, 2018, Torino, Italy.
35. L. Abeni, A. Balsini, T. Cucinotta. "[Container-Based Real-Time Scheduling in the Linux Kernel](#)," in Proceedings of the International Workshop on Embedded Operating Systems (EWILI 2018), October 10th, 2018, Torino, Italy.
36. T. Cucinotta, L. Abeni, M. Marinoni, A. Balsini and C. Vitucci. "[Virtual Network Functions as Real-Time Containers in Private Clouds](#)," in Proceedings of the 11th IEEE International Conference on Cloud Computing (IEEE CLOUD 2018), July 2-7, 2018, San Francisco, CA, USA.
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ORGANIZZAZIONE, DIREZIONE E COORDINAMENTO DI CENTRI O GRUPPI DI RICERCA NAZIONALI E INTERNAZIONALI O PARTECIPAZIONE AGLI STESSI

(per ciascuna voce inserire anno, ruolo, gruppo di ricerca, ecc.)

- Luglio 2019 - oggi: Coordinatore dell'area Real-Time & Embedded Systems presso l'Istituto TECIP della Scuola Superiore Sant'Anna
- Gen 2020 - Giu 2023: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per il progetto di ricerca Europeo AMPERE (H2020 ICT 871669)
- Dic 2016 - Mag 2017: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per il progetto di ricerca "Sched_Deadline, progettazione e sviluppo di alcune estensioni sperimentali allo scheduler real-time per il supporto di alcuni use-case di ARM", commissionato da ARM Ltd., Cambridge, UK.
- Mar 2017 - Mar 2018: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per il progetto di ricerca "Virtualized LTE/GSM packet processing: performance modelling and control on Linux", commissionato da Ericsson AB, Stockholm, Sweden
- Gen 2018 - Set 2018: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per il progetto di ricerca "Sched_Deadline improvements #2 for the Linux kernel, in the two main areas of capacity awareness and energy awareness", commissionato da ARM Ltd., Cambridge, UK
- Apr 2018 - oggi: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per diversi progetti di ricerca commissionati annualmente e con continuità da Vodafone Italia, Milano: "Artificial Intelligence to support Planning of Virtualized Network Functions", "Artificial Intelligence to support Planning of Virtualized Network Functions II", "Artificial Intelligence to support Anomaly Detection in Virtualized Network Functions"
- Apr 2018 - Mar 2019: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per il progetto di ricerca "Definition, design and Optimization of Inter-containers communication paths over a SDN-NFV environment", commissionato da Ericsson AB, Stockholm, Sweden
- Feb 2008 - Gen 2011: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per il progetto di ricerca Europeo IRMOS (FP7 ICT 214777)
- Feb 2010 - Gen 2012: Responsabile scientifico dell'unità di ricerca della Scuola Superiore Sant'Anna per il progetto di ricerca Europeo S(o)OS (FP7 FET 248465)

ATTIVITÀ QUALI LA DIREZIONE O LA PARTECIPAZIONE A COMITATI EDITORIALI DI RIVISTE SCIENTIFICHE

(per ciascuna voce inserire anno, ruolo, rivista scientifica, ecc.)

Partecipazione a comitati editoriali di special issue di riviste scientifiche internazionali

1. Giugno 2020 - Marzo 2022: Guest Editor of the Special Issue of the Elsevier Journal of Systems Architecture (JSA) on "2020 IEEE Symposium on Real-time Distributed Computing (ISORC'20)", 2022
2. Gennaio 2013 - Dicembre 2014: Guest Editor of the Special Issue of the Elsevier Journal of Systems Architecture (JSA) on "Real-time and distributed computing in emerging applications (REACTION)", June 2015
3. Gennaio 2012 - Dicembre 2012: Guest Editor of the Special Issue of the Elsevier Journal of Systems Architecture (JSA) on "Analysis Tools and Methodologies for Embedded and Real-time Systems (WATERS)", 2013
4. Gennaio 2013 - Dicembre 2013: Guest Editor of the Special Issue of the Elsevier Journal of Systems Architecture (JSA) on "Analysis Tools and Methodologies for Embedded and Real-time

Systems (WATERS)", 2014

Partecipazione all'organizzazione di convegni, conferenze e workshop internazionali

1. General Co-Chair of the 17th International Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2022), June 2nd 2022, Hamburg, Germany, co-located with ISC-HighPerformance 2022
2. Program Co-Chair of the 23rd IEEE International Symposium on Real-Time Distributed Computing (ISORC 2020), May 12-14 2019, Nashville, USA
3. General Co-Chair of the 4th International Workshop on Power Management and Scheduling in the Linux Kernel (OSPM Summit), May 11-13 2020, Pisa (Italy)
4. General Co-Chair of the 3rd International Workshop on Power Management and Scheduling in the Linux Kernel (OSPM Summit), May 20-22 2020, Pisa (Italy)
5. General Co-Chair of the 2nd International Workshop on Power Management and Scheduling in the Linux Kernel (OSPM Summit), April 16-18 2018, Pisa (Italy)
6. General Co-Chair of the 1st International Workshop on Power Management and Scheduling in the Linux Kernel (OSPM Summit), April 3-4 2017, Pisa (Italy)
7. General Co-Chair of the 9th International Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2014), August 25-29 2014, Porto, Portugal, co-located with Euro-Par 2014
8. General Co-Chair of the 2nd International Workshop on Real-Time and Distributed Computing in Emerging Applications (REACTION 2013), December 2013, Vancouver, Canada, co-located with IEEE RTSS 2013
9. General Co-Chair of the 4th International Workshop on Analysis Tools and Methodologies for Embedded and Real-time Systems (WATERS 2013), July 2013, Paris, France, co-located with ECRTS 2013
10. General Co-Chair of the International Workshop on Real-Time and Distributed Computing in Emerging Applications (REACTION 2012), December 2012, San Juan, Puerto Rico, co-located with IEEE RTSS 2012
11. General Co-Chair of the 3rd International Workshop on Analysis Tools and Methodologies for Embedded and Real-time Systems (WATERS 2012), July 2012, Pisa, Italy, co-located with ECRTS 2012
12. General Co-Chair of the 2nd International Workshop on Analysis Tools and Methodologies for Embedded and Real-time Systems (WATERS 2011), July 2011, Porto, Portugal, co-located with ECRTS 2011
13. Program Co-Chair of the Fourth IEEE International Workshop on Real-Time Service-Oriented Architecture and Applications (RTSOAA 2011), December 2011, Irvine, CA, co-located with IEEE SOCA 2011.
14. General Co-Chair of the 1st International Workshop on Analysis Tools and Methodologies for Embedded and Real-time Systems (WATERS 2010), July 2010, Brussels, Belgium, co-located with ECRTS 2010

TITOLARITÀ DI BREVETTI

(per ciascun brevetto, inserire autori, titolo, tipologia, numero brevetto, ecc.)

Brevetti concessi (patent grants)

1. E. Jul, D. Cherubini, T. Cucinotta, D. Lugones. "[Method for Balancing a Load, a System, an Elasticity Manager and a Computer Program Product](#)," Patent Grant US9891941B2 February 13th 2018 (application EP14306301 August 20th 2014).
2. T. Cucinotta, E. Jul. "[Load-balancing and scaling of cloud resources by migrating a data session](#)," Patent Grant EP3046028B1 February 19th 2020, US10956190B2 March 23rd, 2021 (application EP15305031 January 15th 2015).
3. T. Cucinotta, Stéphane Betgé-Brezetz. "[Method and system for controlling the exchange of Privacy-Sensitive Information](#)," Patent Grant US10237057B2 March 19th 2019, JP6590807B2 October 16th 2019 (application EP20130360029, September 13th 2013).
4. T. Cucinotta, Marcel Karnstedt. "[Method for Performing Load Balancing, Distributed Computing Environment and Computer Program Product](#)," Patent Grant EP2940580B8 May 2nd 2018 (application EP2014305650 April 30th 2014)
5. T. Cucinotta. "[Method of preventing access to sensitive data of a computing device](#)," Patent Grant US10410004B2 Sep 10th 2019 (application EP20130305396 March 28th 2013).
6. T. Cucinotta, F. Chang, R. Viswanathan. "[Exploiting Probabilistic Latency Expressions For](#)

[Placing Cloud Applications](#)," Patent Grant US9722930B2 August 1st 2017 (application 14/075727 November 8th 2013).

7. E. Jul, T. Cucinotta, D. Cherubini. "[Secure Data Processing](#)," Patent Grant EP2827276B1, July 3rd 2019 (application EP20130306038 July 19th 2013).
8. T. Cucinotta, D. Cherubini, E. Jul. "[Apparatus and method for secure data processing](#)," Patent Grant EP2672673B1 May 25th 2016, US9674153B2 June 6th 2017, CN104335548B March 6th 2018 (application EP20120360045, June 7th, 2012).

Brevetti depositati (filed patents)

1. T. Cucinotta, M. Vannucci, G. Lanciano, F. Galli, F. Brau, A. Artale, E. Sposato, L. N. P. Jorge. "*Metodo per la gestione di risorse di un'infrastruttura per la network function virtualization*," Filed IT Patent 102020000031034, December 2020.
2. T. Cucinotta, M. Vannucci, A. Ritacco, G. Lanciano, A. Artale, J. Barata, E. Sposato. "*Metodo per predire l'evoluzione temporale di una pluralità di dati relativi ad un'infrastruttura telefonica per la network function virtualization*," Filed IT Patent 102019000014262, August 2019.
3. T. Cucinotta, M. Vannucci, A. Ritacco, G. Lanciano, A. Artale, J. Barata, E. Sposato. "*Metodo per identificare e classificare le modalità comportamentali di una pluralità di dati relativi ad un'infrastruttura telefonica per la network function virtualization*," Filed IT Patent 102019000014241, August 2019.
4. T. Cucinotta, D. Cherubini, E. Jul. "*Secure Data Processing*," Filed US Patent 14/405894. December 2014.
5. T. Cucinotta, D. Cherubini, E. Jul. "*Secure Data Processing*," Filed US Patent 14/399372. November 2014.
6. T. Cucinotta, A. Meroni, V. Hilt. "*State Migration for Elastic Virtualized Components*," Filed US Patent 14/338502. July 2014.
7. T. Cucinotta. "*Method for Managing User Requests in a Distributed Computing Environment, Distributed Computing Environment and Computer Program Product*," Filed European Patent 14305652. April 2014.
8. T. Cucinotta. "*Method For Virtual Machine Instantiation, Scalable Computing System, Virtual Machine And Computer Program Product*," Filed European Patent 14305558. April 2014.
9. T. Cucinotta, I. Bedini. "*Method, Processing Framework, Aggregator And Computer Program Product For Processing Data*," Filed European Patent 20130360033. October 2013.
10. T. Cucinotta. "*Method and System for Graphical User Interface Layout Generation, Computer Program Product*," Filed European Patent 13360015. August 2013.
11. T. Cucinotta, A. Sala. "*Methods and Devices for Protecting Private Data*," Filed US Patent 13/944964. July 2013.
12. T. Cucinotta, "*Methods and Devices for Controlling Access to Distributed Resources*," Filed US Patent 13/926832. June 2013.
13. T. Cucinotta, Ivan Bedini. "*Method for Data Processing, Element to Process Data, Method for Monitoring Activity and Computer Program Product*," Filed European Patent 13360010. May 2013.
14. T. Cucinotta. "*Scheduling*," Filed European Patent 20130305397. March 2013.
15. I. Bedini, T. Cucinotta, A. Sala, B. Theeten. "*Systems And Methods For Self-Adaptive Distributed Systems*," Filed US Patent 13/853533. March 2013.
16. T. Cucinotta, D. Cherubini, E. Jul. "*Collaborative Uses of a Cloud Computing Confidential Domain of Execution*," Filed US Patent 13/622007. September 2012.
17. T. Cucinotta, D. Cherubini. "*System Control*," Filed European Patent 12360068. September 2012.
18. T. Cucinotta, D. Cherubini, E. Jul. "*Secure Data Processing*," Filed European Patent 20120360044. June 2012.
19. T. Cucinotta. "*Task Scheduling*," Filed European Patent 12360037. May 2012

PREMI E RICONOSCIMENTI NAZIONALI E INTERNAZIONALI PER ATTIVITÀ DI RICERCA (inserire premio, data, ente organizzatore, ecc.)

- April 2021: [Best Student Paper Award](#) for co-authored paper at CLOSER 2021
- May 2020: [Best Paper Award](#) for co-authored paper at CLOSER 2020
- February 2020: [Best Paper Award](#) for co-authored paper at ICN 2020
- March 2019: [Best Paper Award](#) for co-authored paper at ICN 2019
- April 2018: [Best Paper Award](#) for co-authored paper at ICN 2018
- July 2016: Certificate of Outstanding Contribution in Reviewing for Elsevier Journal of Computer

and System Sciences

- **April 2014:** Selected as [featured researcher profile](#) in highlight on the Bell Labs website
- **October 2013:** [IEEE Senior Member grade](#) elevation
- **December 2010:** [Best-paper award](#) for co-authored paper at IEEE SOCA 2010

PARTECIPAZIONE IN QUALITÀ DI RELATORE A CONGRESSI E CONVEGNI DI INTERESSE INTERNAZIONALE
(*inserire titolo congresso/convegno, data, ecc.*)

1. "Predictable Cloud Computing Enhanced by Real-Time Scheduling in the Linux Kernel," International Workshop on Cloud Native Real Time Systems (CLONAR), at IEEE ETFA, Stuttgart, Sep 2022
2. "Model-based FPGA design flow for real-time applications: a preliminary case study," FORECAST workshop at HiPEAC, Budapest, June 2022
3. "On the use of Linux Real-Time Features for RAN Packet Processing in Cloud Environments," 17th Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2022), International Supercomputing Conference - High Performance (ISC 2022), June 2nd, 2022, Hamburg, Germany.
4. "Differentiated performance in NoSQL database access for Cloud HPC workloads," 16th Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2021), International Supercomputing Conference - High Performance (ISC 2021), July 2nd, 2021, Frankfurt, Germany (on-line event due to Covid-19).
5. "An Evaluation of Adaptive Partitioning of Real-Time Workloads on Linux," 24th IEEE International Symposium on Real-Time Distributed Computing (IEEE ISORC 2021), June 1-3, 2021, Daegu, South Korea (on-line event due to Covid-19).
6. "RT-MongoDB: a NoSQL database with differentiated performance," 11th International Conference on Cloud Computing and Services Science (CLOSER 2021), April 28-30, 2021, Prague, Czech Republic (on-line event due to Covid-19).
7. "Migrating Constant Bandwidth Servers on Multi-Cores," 29th International Conference on Real-Time Networks and Systems (RTNS 2021), April 7-9, 2021, Nantes, France (on-line event due to Covid-19).
8. "On the use of kernel bypass mechanisms for high-performance inter-container communications," 14th Workshop on Virtualization in High-Performance Cloud Computing (VHPC 2019), International Supercomputing Conference - High Performance (ISC 2019), June 20th, 2019, Frankfurt, Germany.
9. D. B. de Oliveira, R. S. de Oliveira, T. Cucinotta. "Untangling the Intricacies of Thread Synchronization in the PREEMPT RT Linux Kernel," 22nd IEEE International Symposium on Real-Time Distributed Computing (IEEE ISORC 2019), May 7-9, 2019, Valencia, Spain.
10. "Modeling and simulation of power consumption and execution times for real-time tasks on embedded heterogeneous architectures," International Workshop on Embedded Operating Systems (EWILI 2018), October 10th, 2018, Torino, Italy.
11. "Modeling the Behavior of Threads in the PREEMPT_RT Linux Kernel Using Automata," International Workshop on Embedded Operating Systems (EWILI 2018), October 10th, 2018, Torino, Italy.
12. "Container-Based Real-Time Scheduling in the Linux Kernel," International Workshop on Embedded Operating Systems (EWILI 2018), October 10th, 2018, Torino, Italy.
13. "Virtual Network Functions as Real-Time Containers in Private Clouds," 11th IEEE International Conference on Cloud Computing (IEEE CLOUD 2018), July 2-7, 2018, San Francisco, CA, USA.
14. "Automata-Based Modeling of Interrupts in the Linux PREEMPT RT Kernel," 22nd IEEE International Conference on Emerging Technologies And Factory Automation (ETFA 2017), September 12-15, 2017, Limassol, Cyprus.
15. "Challenges in Operating System Design for Future Many-Core Systems," Workshop on Trends, Issues and Challenges in Future High-End Computing, All Hands Meeting (AHM 2010), Cardiff (UK), September 2010
16. "Probabilistic Admission Control for Elastic Cloud Computing," IEEE International Conference on Service-Oriented Computing and Applications (SOCA 2011), December 12-14 2011, Irvine, CA.
17. "A Heuristic for Optimum Allocation of Real-Time Service Workflows," IEEE International Conference on Service-Oriented Computing and Applications (SOCA 2011), December 12-14 2011, Irvine, CA
18. "Efficient Virtualisation of Real-Time Activities," IEEE International Workshop on Real-Time Service-Oriented Architecture and Applications (RTSOAA 2011), December 12-14 2011, Irvine, CA.

19. "A QoS Registry for Adaptive Real-Time Service-Oriented Applications," IEEE International Workshop on Real-Time Service-Oriented Architecture and Applications (RTSOAA 2011), December 12-14 2011, Irvine, CA.
20. "Optimum Scalability Point for Parallelisable Real-Time Components," International Workshop on Synthesis and Optimization Methods for Real-time and Embedded Systems (SOMRES 2011), co-located with the 32nd IEEE Real-Time Systems Symposium (RTSS 2011), Vienna, Austria, November 29 - December 2, 2011.
21. "Improving Responsiveness for Virtualized Networking Under Intensive Computing Workloads," 13th Real-Time Linux Workshop (RTLWS 2011), Prague, Czech Republic, October 2011.
22. "A Latency Simulator for Many-core Systems," 44th Annual Simulation Symposium (ANSS 2011), part of the Spring Simulation Multiconference (SpringSim'11).
23. "Low-Latency Audio on Linux by Means of Real-Time Scheduling," Linux Audio Conference (LAC 2011), Maynooth, Ireland, May 2011.
24. "Virtualised e-Learning with Real-Time Guarantees on the IRMOS Platform," IEEE International Conference on Service-Oriented Computing and Applications (SOCA 2010), Perth, Australia, December 2010.
25. "The IRMOS Real-Time Scheduler," Real-Time Linux Workshop 2010, Strathmore University, Nairobi, Kenya, October 2010
26. "An Exception Based Approach to Timing Constraints Violations in Real-Time and Multimedia Applications," IEEE Symposium on Industrial Embedded Systems (SIES 2010), Trento, Italy, July 2010.
27. "Advance Reservations for Distributed Real-Time Workflows with Probabilistic Service Guarantees," IEEE International Conference on Service-Oriented Computing and Applications (SOCA 2009), Taipei, Taiwan, December 2009.
28. "The Wizard of OS: a Heartbeat for Legacy Multimedia Applications," in Proceedings of the 7th IEEE Workshop on Embedded Systems for Real-Time Multimedia (ESTImedia 2009), Grenoble, France, October 2009.
29. "Weighted Feedback Reclaiming for Multimedia Applications," in Proceedings of the 6th IEEE Workshop on Embedded Systems for Real-Time Multimedia (ESTImedia 2008), Atlanta, Georgia, United States, October 2008.
30. "Real-Time Virtual Machines," 29th Real-Time System Symposium (RTSS 2008) -- Work in Progress Session, Barcelona, December 2008.
31. "Access Control for Adaptive Reservations on Multi-User Systems," 14th IEEE Real-Time and Embedded Technology and Applications Symposium (RTAS 2008), St. Louis, MO, United States, April 2008.
32. "A protocol for programmable smart cards," Trust and Privacy in Digital Business Workshop, Prague, Czech Republic, September 2003. 14th International Workshop on Database and Expert Systems Applications (DEXA 2003), IEEE Computer Society

ATTIVITÀ GESTIONALI, ORGANIZZATIVE E DI SERVIZIO

INCARICHI DI GESTIONE E AD IMPEGNI ASSUNTI IN ORGANI COLLEGIALI E COMMISSIONI, PRESSO RILEVANTI ENTI PUBBLICI E PRIVATI E ORGANIZZAZIONI SCIENTIFICHE E CULTURALI, OVVERO PRESSO L'ATENEO O ALTRI ATENEI

(inserire incarico/impegno, ente, data, ecc.)

- **2021/22, 2022/23: Board Member** of the National PhD in Artificial Intelligence - AI & Society branch at Scuola Superiore Sant'Anna, jointly offered with University of Pisa, Politecnico di Torino, National Research Council and others
- **2017/18, 2018/19, 2019/20, 2020/21: Board Member** of the PhD in Data Science, joint program at Scuola Superiore Sant'Anna, Scuola Normale Superiore, University of Pisa, National Research Council and Scuola IMT Lucca
- **2016/17, 2022/23: Board Member** of the PhD in Emerging Digital Technologies at Scuola Superiore Sant'Anna
- **Nov 2021: Membro Commissione Concorso per RTDA ING-INF/05** presso l'Università di Catania
- **2020/21, 2021/22: Membro della Commissione Paritetica**, Scuola Superiore Sant'Anna
- **2019 - oggi: Membro del Consiglio Direttivo** del Consorzio Interuniversitario Nazionale per l'Informatica (CINI), in rappresentanza della Scuola Superiore Sant'Anna
- **Set 2021 - Nov 2021: Membro Commissione Concorso per RTDA ING-INF/05** presso il Dipartimento d'Ingegneria dell'Informazione dell'Università di Pisa

- **Ago 2021 - Set 2021: Membro Esterno Commissione Licentiat Thesis in Computer Science** di Vaclav Struhar presso Maelardalen University, Vasteras, Sweden
- **Mag 2019: Membro Commissione Concorso per PTA Cat.D**, Dipartimento d'Eccellenza EMBEDS, Scuola Superiore Sant'Anna
- **Giu 2018: Membro Esterno Commissione PhD Defense** Bernardo Rabindranath Villalba Frias presso l'Università di Trento
- **Mag 2012: Membro Esterno Commissione Licentiat Thesis in Computer Science** di Michael Aesberg presso Department of Computer Science and Engineering, Maelardalen University, Vasteras, Sweden
- **Nov 2011 - Dic 2011: Membro Esterno Commissione Licentiat Thesis in Computer Science** di Rafia Inam presso Maelardalen University, Vasteras, Sweden

Data

5/09/2022

Luogo

Pisa